SGS-THOMSON
MICROELECTRONICS

FUZZY LOGIC PRODUCTS

W.A.R.P. FAMILY - HCMOS FUZZY DEDICATED MICROPROCESSORS

The W.A.R.P. family is made up of high performance VLSI fuzzy logic microprocessors operating with linguistic variable and human reasoning. It is aimed at top end applications in high speed control system working stand-alone or like a coprocessor at a frequency up to 40MHz.

Type	Description	Operating Temperature	Package
STFLWARP11/PG-PL	HCMOS, 6Kbytes RAM, 40 MHz 16 Inputs, 16 Outputs, 256 Rules	0 - 70 °C	CPGA100-PLCC84
STFLWARP20/PL	HCMOS, 1.4Kbytes RAM, 40 MHz, 8 Inputs, 4 Outputs, 256 Rules	0 - 70 °C	PLCC68

W.A.R.P. FAMILY - DEVELOPMENT TOOLS

FUZZYSTUDIO™ is the development tool consisting of the Application Development Board (ADB) and the Software Development Tool (SDT). ADB includes; W.A.R.P. programmer, EPROM programmer, RS-232 Serial comunication handler and internal clock. SDT includes W.A.R.P. compiler, source level Debugger, and Exporter for ANSI C and MATLAB® environment.

Adaptive Fuzzy Modeller is an high level software tool based on neural networks that automatically provides the fuzzy model of a real system starting from a sampling of it.

Type	Device	Development Tools	
		FUZZYSTUDIO™ ADB	FUZZYSTUDIO™ SDT
STFLSTUDIO10/KIT	STFLWARP11/PG	W.A.R.P. 1.X W.A.R.P. 1.X programmer EPROM programmer RS-232 communication handler Internal Clock	Variables and Rules Editor W.A.R.P. Compiler/Debugger Exporter for ANSI C and MATLAB®

Type	Description	Supported Target	Functionalities	System Requirement
STFLAFM10/SW	WTA-FAM for Building Rules BACK-FAM for Building MFs	STFLWARP11/PG STFLWARP11/PL STFLWARP20/PL ANSI C MATLAB®	Rules Minimizer Step-by-Step Simulation Simulation from File Local Tuning	MS-DOS 3.1 or higher Windows 3.0 or later 486, PENTIUM compatible 8 MB RAM